How to change DS18B20 temperature to 12bit resolution with using MERVIS environment

1. start new project

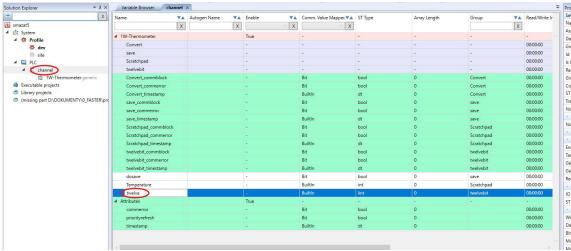
- 2. add 1Wire channel
- 3. assign DS18B020 thermometer
- 4. add new group in "channel" name "twelvebit"

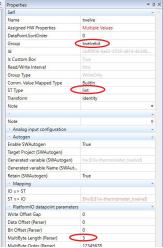
ution Explorer 🛛 🔻 🖡 🗙	Variable Browser channel	×							Properties	₩ 1
X	Name 🔻	Autogen Name 🗸	Enable 🗸	Comm. Value Mapper VA	ST Type	Array Length	Group 🗸	Read/Write Ir	Group Properties	-
smazat5	X	X	~ X	~ X			X	1	Name	twelvebit
📑 System	▲ 1W-Thermometer		True					2	Assigned HW Properties	Multiple Values
4 🌞 Profile				-	-	-		-	DataPoint.SortOrder	0
🄅 dev	Convert		•	•	-	•	•	00:00:00	Id	86271598-44f5-4bdd-a9be-6aae7
ŵ site	save		-	2	-	-	-	00:00:00	Is Custom Box	True
A 🛄 PLC	Scratchpad		•	-	-	•	· ·	00:00:00	Read/Write Interval	Oms
4 Channel	twelvebit		-					00:00:00	Group Type	WriteOnly
1W-Thermometer.generic	Convert_commblock		-	Bit	bool	0	Convert	00:00:00	Note	
Executable projects	Convert_commerror		•	Bit	bool	0	Convert	00:00:00	PlatformIO group parameters	
Library projects	Convert_timestamp		-	BuiltIn	dt	0	Convert	00:00:00		78
(missing part D:\DOKUMENTY\0_FASTER\prc	save_commblock			Bit	bool	0	save	00:00:00	Command Length	
	save_commerror			Bit	bool	0	save	00:00:00		3
	save_timestamp		-	BuiltIn	dt	0	save	00:00:00	Send as Broadcast	False
	Scratchpad_commblock		-	Bit	bool	0	Scratchpad	00:00:00	Enable CRC Check	False
	Scratchpad_commerror			Bit	bool	0	Scratchpad	00:00:00	Input filter mask [hex]	
	Scratchpad_timestamp			BuiltIn	dt	0	Scratchpad	00:00:00	Input expected mask [hex]	
	twelvebit_commblock		-	Bit	bool	0	twelvebit	00:00:00		
	twelvebit_commerror		1	Bit	bool	0	twelvebit	00:00:00	Logical OR over Overlapped	False
	twelvebit_timestamp			BuiltIn	dt	0	twelvebit	00:00:00	Note	-
	dosave		-	Bit	bool	0	save	00:00:00		
	Temperature			BuiltIn	int	0	Scratchpad	00:00:00		
	twelve			Builtin	lint	0	twelvebit	00:00:00		
	▲ Attributes		True	-	1000					
	commerror			Bit	bool	0		00:00:00		
	priorityrefresh			Bit	bool	0		00:00:00		
	timestamp		1	Builtin	dt	0		00:00:00		
	umestamp		15	Dullun	u	U		00.00.00		

5. add new group in "channel" - name "save"

on Explorer 🔹 🖣 🗙	Variable Browser channel			100 N 100 N 100 N	AN LE PERSON A LA SUMMER A	100 0.0000	100000		Properties Group Properties	
X		Autogen Name	▼▲ Enable	VA Comm. Value M	appec 🗛 ST Type	Array Length	Group	▼▲ Read/Write Ir	Name	save
nazat5	X		х	~ X	~ X			x	Assigned HW Properties	Multiple V
System	4 1W-Thermometer		True	2					DataPoint.SortOrder	0
🔅 Profile	Convert		-		-		-	00:00:00	Id	548b3f1c-
i site	save		-	-	-	-	-	00:00:00	Is Custom Box	True
PLC	Scratchpad		-		-		-	00:00:00	Read/Write Interval	Oms
4 Channel	twelvebit		-	2	-	-		00:00:00	Group Type	WriteOnh
1W-Thermometer.generic	Convert_commblack		-	Bit	bool	0	Convert	00:00:00	Note	
ixecutable projects	Convert_commerror			Bit	bool	0	Convert	00:00:00	PlatformIO group parameter	s
ibrary projects	Convert_timestamp		-	BuiltIn	dt	0	Convert	00:00:00	Command	72
missing part D:\DOKUMENTY\0_FASTER\pro	save_commblock		-	Bit	bool	0	save	00:00:00	Command Length	\bigcirc
	save_commerror		-	Bit	bool	0	save	00:00:00	Data Length	0
	save_timestamp			BuiltIn	dt	0	save	00:00:00	Send as Broadcast	False
	Scratchpad_commblock		-	Bit	bool	0	Scratchpad	00:00:00	Enable CRC Check	False
	Scratchpad_commerror			Bit	bool	0	Scratchpad	00:00:00	Input filter mask [hex]	
	Scratchpad timestamp		-	BuiltIn	dt	0	Scratchpad	00:00:00	Input expected mask [hex]	
	twelvebit_commblock			Bit	bool	0	twelvebit	00:00:00	Write Only On Change	True
	twelvebit commerror		-	Bit	bool	0	twelvebit	00:00:00	Logical OR over Overlapped	False
	twelvebit_timestamp			BuiltIn	dt	0	twelvebit	00:00:00	Note	
	dosave		-	Bit	bool	0	save	00:00:00		
	Temperature			BuiltIn	int	0	Scratchpad	00:00:00		
	twelve		4	BuiltIn	lint	0	twelvebit	00:00:00		
	∡ Attributes		True	-	-	-	-	-		
	commerror		-	Bit	bool	0		00:00:00		
	priorityrefresh		-	Bit	bool	0		00:00:00		
	timestamp		1	BuiltIn	dt	0		00:00:00		

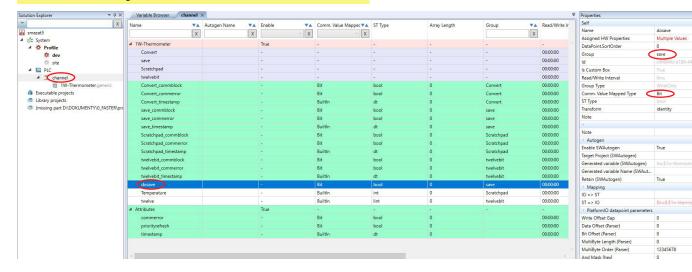
6. add new "data point" in "channel" - name "twelve"





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7. add new "data point" in "channel" - name "doSave"



- II >

8. build and run - after writing 127 the DS18B20 run in 12bit resolution mode / till switch OFF/

m X	Name X	Namesp VA	Module VA	Type 🔽	Kind 🔽	PLC Value	PLC Last Comm
System	1W-Thermometer_Scratchpad_commblock	hw	smazat5	bool	Global	False	1/19/2018 1:58:00
 Profile dev site 	1W-Thermometer_Scratchpad_timestamp		smazat5	dt	Global	dt#2018-01-19-12:58:00	1/19/2018 1:58:00
	1W-Thermometer_Scratchpad_commerror		smazat5	bool	Global	False	1/19/2018 1:58:00
A Street	1W-Thermometer_Convert_commblock	hw	smazat5	bool	Global	False	1/19/2018 1:58:00
Channel	1W-Thermometer_Convert_timestamp	hw	smazat5	dt	Global	dt#2018-01-19-12:58:00	1/19/2018 1:58:00
1W-Thermometer.generic	1W-Thermometer_Convert_commerror	hw	smazat5	bool	Global	False	1/19/2018 1:58:00
 Executable projects image in a smazat5 image in a smazat5 	1W-Thermometer_Temperature	hw	smazat5 real	Global	30.8125	1/19/2018 1:58:00	
	1W-Thermometer_dosave	hw	smazat5	bool	Global	False	1/19/2018 1:57:08
	1W-Thermometer_twelve	hw	smazat5	lint	Global 🤇	127	1/19/2018 1:56:58
Globals	channel.1W-Thermometer.Temperature	HW_hidden	PLC	int	Global	493	1/19/2018 1:58:00
 main.Program.fbd Library projects 	tmp_real	HW_hidden	PLC	real	Global	30.8125	1/19/2018 1:58:00
is cionary projects	channel.1W-Thermometer.Temperature.transformation	HW_hidden	PLC	Linear	Global	{eno=0,en=1,k=0.0625,q	=0} 1/19/2018 1:58:00
			d	ec	hex	resolution	conversion time
			(127	#7F	12 bit	93,75ms
				95	#5F	11 bit	185,5ms
				63	#3F	10 bit	375ms
				31	#1F	9 bit	750ms

9. for permanent change - set "doSave" to "True"

X	Name 🗸	Namesp 🔻 🔺	Module V	Туре 🔻	Kind 🔽	PLC Value	PLC Last Comm
smazat5	X	×	x	×			
System	1W-Thermometer_Scratchpad_commblock	hw	smazat5	bool	Global	False	1/19/2018 1:59:12 PM
Profile dev	1W-Thermometer_Scratchpad_timestamp	hw	smazat5	dt	Global	dt#2018-01-19-12:59:12	1/19/2018 1:59:12 PM
i site	1W-Thermometer_Scratchpad_commerror	hw	smazat5	bool	Global	False	1/19/2018 1:59:12 PM
✓ Site	1W-Thermometer_Convert_commblock	hw	smazat5	bool	Global	False	1/19/2018 1:59:12 PM
 channel 	1W-Thermometer_Convert_timestamp	hw	smazat5	dt	Global	dt#2018-01-19-12:59:12	1/19/2018 1:59:12 PM
1W-Thermometer.generic	1W-Thermometer_Convert_commerror	hw	smazat5	bool	Global	False	1/19/2018 1:59:12 PM
Executable projects	1W-Thermometer_Temperature	hw	smazat5	real	Global	28.75	1/19/2018 1:59:12 PM
🔺 🚞 smazat5	1W-Thermometer_dosave	hw	smazat5	bool	Global	True	1/19/2018 1:59:04 PM
References	1W-Thermometer_twelve	hw	smazat5	lint	Global	127	1/19/2018 1:56:58 PM
Globals	channel.1W-Thermometer.Temperature	HW hidden	PLC	int	Global	460	1/19/2018 1:59:12 PM
main.Program.fbd	tmp_real	HW hidden	PLC	real	Global	28.75	1/19/2018 1:59:12 PM
Library projects	channel.1W-Thermometer.Temperature.transformation	HW hidden	PLC	Linear	Global	{eno=0,en=1,k=0.0625,g=0}	1/19/2018 1:59:12 PM
		The filled	Pic .	Linear	GIODAI	{eno=0,en=1,k=0.0623,q=0}	1/19/2010 1.55.12 PW
						port monitor" vi	
	Port Monitor						
	Port Monitor Controller PLC Channel Status	The					
		The					

MEMORY

The DS18B20's memory is organized as shown in Figure 7. The memory consists of an SRAM scratchpad with nonvolatile EEPROM storage for the high and low alarm trigger registers (T_H and T_L) and configuration register. Note that if the DS18B20 alarm function is not used, the T_H and T_L registers can serve as general-purpose memory. All memory commands are described in detail in the DS18B20 *Function Commands* section.

Byte 0 and byte 1 of the scratchpad contain the LSB and the MSB of the temperature register, respectively. These bytes are read-only. Bytes 2 and 3 provide access to T_H and T_L registers. Byte 4 contains the configuration register data, which is explained in detail in the *Configuration Register* section. Bytes 5, 6, and 7 are reserved for internal use by the device and cannot be overwritten.

Byte 8 of the scratchpad is read-only and contains the CRC code for bytes 0 through 7 of the scratchpad. The DS18B20 generates this CRC using the method described in the *CRC Generation* section.

Data is written to bytes 2, 3, and 4 of the scratchpad using the Write Scratchpad [4Eh] command; the data must be transmitted to the DS18B20 starting with the least significant bit of byte 2. To verify data integrity, the scratchpad can be read (using the Read Scratchpad [BEh] command) after the data is written. When reading the scratchpad, data is transferred over the 1-Wire bus starting with the least significant bit of byte 0. To transfer the T_H , T_L and configuration data from the scratchpad to EEPROM, the master must issue the Copy Scratchpad [48h] command.

Data in the EEPROM registers is retained when the device is powered down; at power-up the EEPROM data is reloaded into the corresponding scratchpad locations. Data can also be reloaded from EEPROM to the scratchpad at any time using the Recall E^2 [B8h] command. The master can issue read time slots following the Recall E^2 command and the DS18B20 will indicate the status of the recall by transmitting 0 while the recall is in progress and 1 when the recall is done.

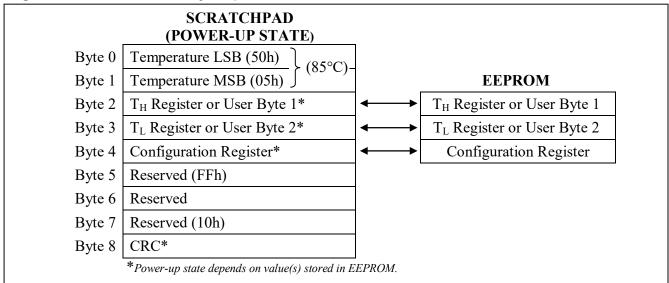


Figure 7. DS18B20 Memory Map

CONFIGURATION REGISTER

Byte 4 of the scratchpad memory contains the configuration register, which is organized as illustrated in Figure 8. The user can set the conversion resolution of the DS18B20 using the R0 and R1 bits in this register as shown in Table 2. The power-up default of these bits is R0 = 1 and R1 = 1 (12-bit resolution). Note that there is a direct tradeoff between resolution and conversion time. Bit 7 and bits 0 to 4 in the configuration register are reserved for internal use by the device and cannot be overwritten.

Figure 8. Configuration Register

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
0	R1			1		1	1

R1	R0	RESOLUTION (BITS)	MAX CONVERSION TIME		
0	0	9	93.75ms	$(t_{\rm CONV}/8)$	
0	1	10	187.5ms	$(t_{\rm CONV}/4)$	
1	0	11	375ms	$(t_{\rm CONV}/2)$	
1	1	12	750ms	$(t_{\rm CONV})$	

CRC GENERATION

CRC bytes are provided as part of the DS18B20's 64-bit ROM code and in the 9th byte of the scratchpad memory. The ROM code CRC is calculated from the first 56 bits of the ROM code and is contained in the most significant byte of the ROM. The scratchpad CRC is calculated from the data stored in the scratchpad, and therefore it changes when the data in the scratchpad changes. The CRCs provide the bus master with a method of data validation when data is read from the DS18B20. To verify that data has been read correctly, the bus master must re-calculate the CRC from the received data and then compare this value to either the ROM code CRC (for ROM reads) or to the scratchpad CRC (for scratchpad reads). If the calculated CRC matches the read CRC, the data has been received error free. The comparison of CRC values and the decision to continue with an operation are determined entirely by the bus master. There is no circuitry inside the DS18B20 that prevents a command sequence from proceeding if the DS18B20 CRC (ROM or scratchpad) does not match the value generated by the bus master.

The equivalent polynomial function of the CRC (ROM or scratchpad) is:

$$CRC = X^8 + X^5 + X^4 + 1$$

The bus master can re-calculate the CRC and compare it to the CRC values from the DS18B20 using the polynomial generator shown in Figure 9. This circuit consists of a shift register and XOR gates, and the shift register bits are initialized to 0. Starting with the least significant bit of the ROM code or the least significant bit of byte 0 in the scratchpad, one bit at a time should shifted into the shift register. After shifting in the 56th bit from the ROM or the most significant bit of byte 7 from the scratchpad, the polynomial generator will contain the re-calculated CRC. Next, the 8-bit ROM code or scratchpad CRC from the DS18B20 must be shifted into the circuit. At this point, if the re-calculated CRC was correct, the shift register will contain all 0s. Additional information about the Maxim 1-Wire cyclic redundancy check